

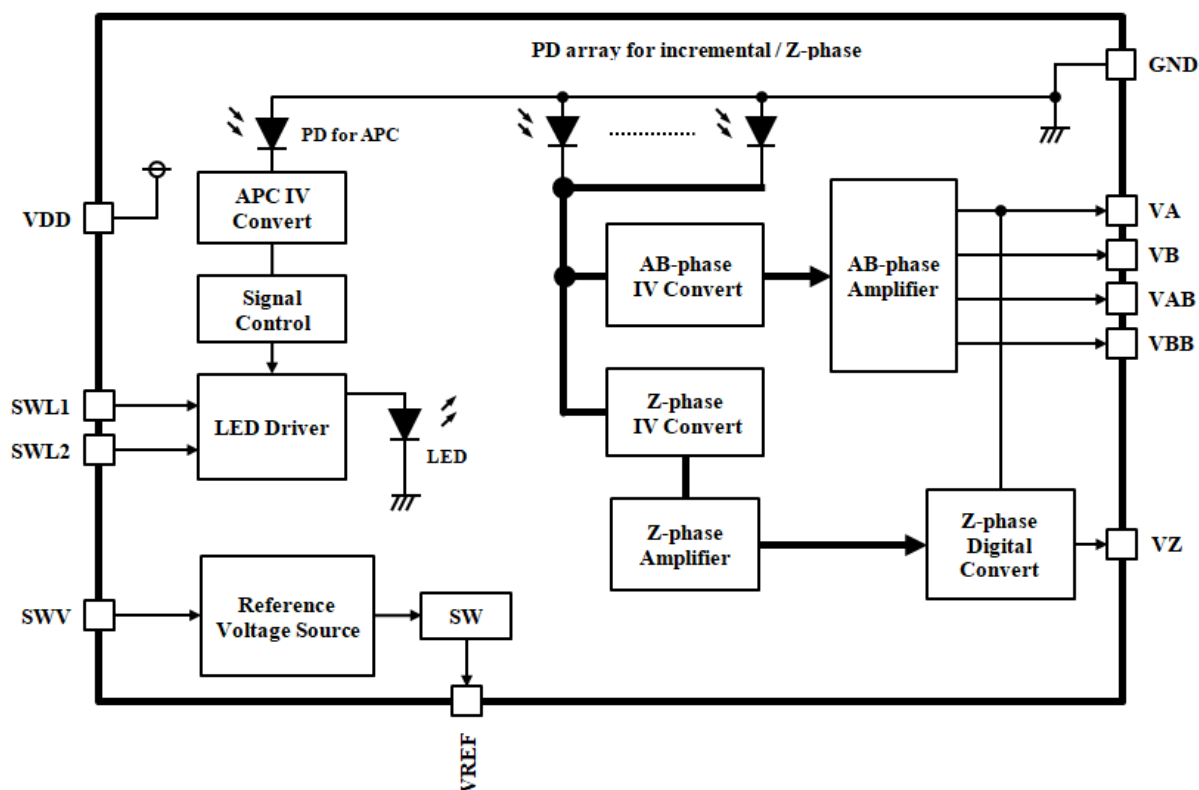
1. OVERVIEW

The SMD-04B is a high precision optical encoder that employs a diffraction image reflection method. It implements an OEIC (Opto-Electric Integrated Circuit) and LED in a single package. Light emitted from the LED is projected onto a scale, and the reflected diffraction image detected by a photodiode detector is used to determine the relative movement between the SMD-04B and the scale. A Z-phase origin signal can also be output by establishing a pattern for origin detection on the scale. It also features a photodiode detector array to reduce degradation in phase characteristics due to variations in mounting accuracy.

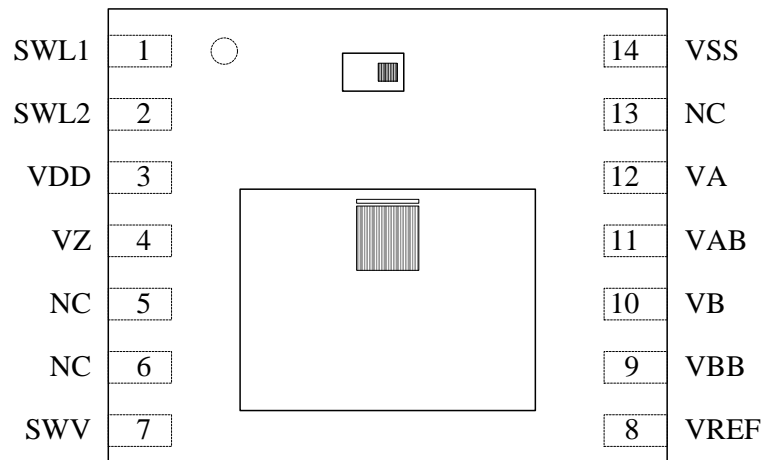
2. FEATURES

- Compact, clear molded package (5.3mm×4.3mm×1.0mm)
- 5μm resolution (generated on A and B phases using scale with 20μm pitch pattern)
- Analog output with 20μm cycle (sine wave) and Z-phase digital output
- LED and OEIC fabricated in a single package
- LED brightness setting and automatic brightness adjustment selectable using external pins
- LED brightness auto power control function to reduce variation in analog output signal amplitude
- 4.5 to 5.25V supply voltage
- 22.0mA current consumption

3. BLOCK DIAGRAM



4. PIN LAYOUT



5. PIN DESCRIPTION

No.	Name	I/O	Function
1	SWL1	I	LED brightness setting input 1 (with pull-up resistance)
2	SWL2	I	LED brightness setting input 2 (with pull-up resistance)
3	VDD	-	Supply voltage
4	VZ	O	Z-phase digital signal output
5	NC	-	No connection (leave open-circuit)
6	NC	-	No connection (leave open-circuit)
7	SWV	I	Reference voltage setting input (with pull-up resistance)
8	VREF	O	Reference voltage output
9	VBB	O	B-phase inverting analog signal (BB phase) output
10	VB	O	B-phase analog signal (B phase) output
11	VAB	O	A-phase inverting analog signal (AB phase) output
12	VA	O	A-phase analog signal (A phase) output
13	NC	-	No connection (leave open-circuit)
14	VSS	-	Ground

6. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Rating	Unit	note
Supply voltage range	V _{DD}	Voltage between VDD and VSS	-0.3 to +6.0	V	*1
Input voltage range	V _{IN}	SWL1 , SWL2 , SWV pins	-0.3 to V _{DD} +0.3	V	*1, *2
Output voltage range	V _{OUT}	VA , VAB , VB , VBB , VREF , VZ pins	-0.3 to V _{DD} +0.3	V	*1, *2
Storage temperature range	T _{STG}	-	-30 to +80	°C	*3

*1: Parameters must not exceed ratings, not even momentarily. If a rating is exceeded, there is a risk of IC failure, deterioration in characteristics, and decrease in reliability.

*2: V_{DD} in absolute value ratings refers to the recommended operating voltage V_{DD} value.

*3: Stored separately in Nitrogen (N₂) atmosphere or vacuum.

7. RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Conditions	V _{SS} =0V			
			MIN	TYP	MAX	Unit
Supply voltage	V _{DD}	Between VDD and VSS terminals	4.5	-	5.25	V
Operating temperature	T _a		-30	-	85	°C
Response speed	R _t		0	-	2	m/s

* Operation outside the recommended operating conditions may adversely affect reliability. Use only within specified ratings.

8. ELECTRICAL CHARACTERISTICS

8.1. DC Characteristics

$V_{DD} = 5V$, $V_{SS} = 0V$, $T_a = 27^{\circ}C$ unless otherwise noted

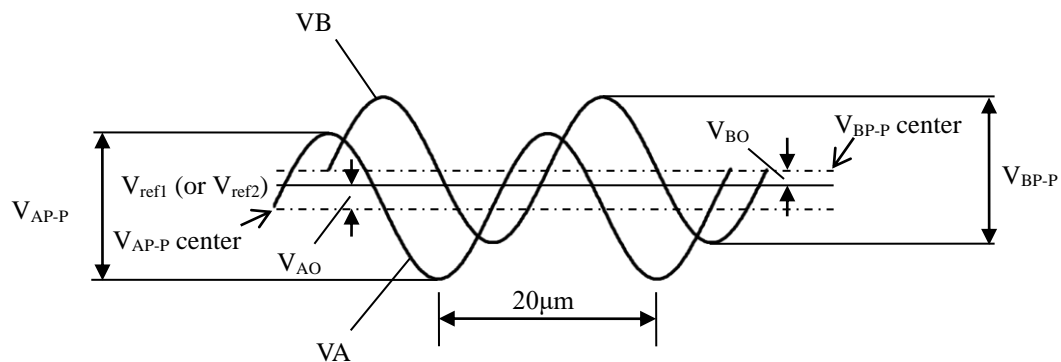
Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit	Pins
Current consumption 1	I_{DD1}	SWL1 = H , SWL2 = H	-	10	16	mA	VDD
Current consumption 2	I_{DD2}	SWL1 = H , SWL2 = L	-	14.2	22		
Current consumption 3	I_{DD3}	SWL1 = L , SWL2 = H	-	22	32		
Current consumption 4	I_{DD4}	SWL1 = L , SWL2 = L	-	5	9		
Reference voltage 1	V_{ref1}	SWV=H	1.1	1.45	1.8	V	VREF
Reference voltage 2	V_{ref2}	SWV=L	1.8	2.25	2.7		
A-phase signal offset voltage	V_{AO}	SWL1 = L , SWL2 = L Variation from VREF	-0.15	-	0.15	V	VA
AB-phase signal offset voltage	V_{ABO}						VAB
B-phase signal offset voltage	V_{BO}						VB
BB-phase signal offset voltage	V_{BBO}						VBB
Output voltage fluctuation 1	V_{D1}	Difference between 0 μ A and 50 μ A sink current	0	-	30	mV	VA VAB VB VBB
Output voltage fluctuation 2	V_{D2}	Difference between 0 μ A and 50 μ A sink current	-30	-	0		
Output voltage fluctuation 3	V_{D3}	Difference between 0 μ A and 50 μ A sink current	0	-	150	mV	VREF
Output voltage fluctuation 4	V_{D4}	Difference between 0 μ A and 50 μ A sink current	-150	-	0		
HIGH-level input voltage	V_{IH}	-	$0.8 \cdot V_{DD}$	-	V_{DD}	V	SWL1 SWL2 SWV
LOW-level input voltage	V_{IL}	-	0	-	$0.2 \cdot V_{DD}$	V	
HIGH-level input current	I_{IH}	$V_{IN} = V_{DD}$	-1	-	1	μ A	
LOW-level input current	I_{IL}	$V_{IN} = V_{SS}$	-20		-1	μ A	
Z-phase signal HIGH-level voltage	V_{OH}	$I_{out} = 0.5mA$, SWV=L	$0.8 \cdot V_{DD}$	-	V_{DD}	V	VZ
Z-phase signal LOW-level voltage	V_{OL}	$I_{out} = -0.5mA$	0	-	$0.2 \cdot V_{DD}$	V	VZ

8.2. Analog Characteristics

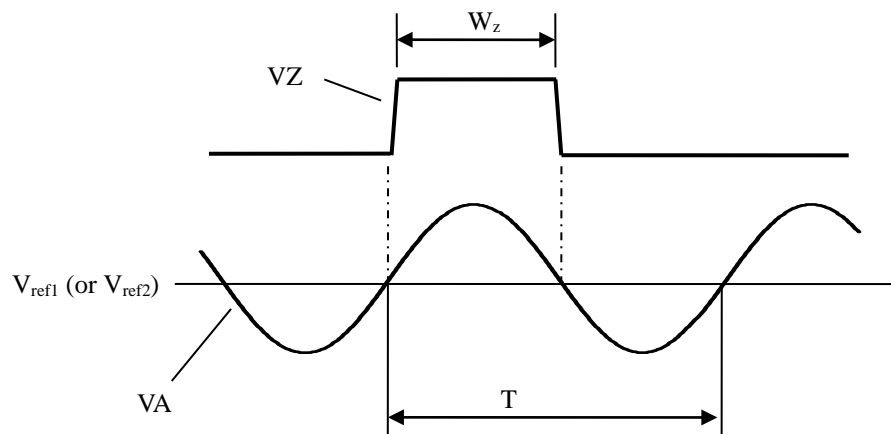
$V_{DD} = 5V$, $V_{SS} = 0V$, $T_a = 27^\circ C$ unless otherwise noted

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit	Pins
A-phase output signal amplitude	V_{AP-P}	SWL1 = L , SWL2 = H Standard scale conditions Value of peak to peak	0.8	1.2	1.92	V	VA
AB-phase output signal amplitude	V_{ABP-P}						VAB
B-phase output signal amplitude	V_{BP-P}						VB
BB-phase output signal amplitude	V_{BBP-P}						VBB
A-phase - B-phase difference	D_p	Standard scale conditions	80	90	100	°	VA,VB
Z-phase signal output width	W_z	Standard scale conditions $T = 20 \mu m^{*1}$	0.17T	0.5T	0.7T	μm	VZ

*1: "T" indicates the width of a single VA and VB cycle.



V_{AO} and V_{BO} indicate the difference between the VA and VB center line and V_{ref1} (or V_{ref2}), respectively.



8.3. AC Characteristics

$V_{DD} = 5V$, $V_{SS} = 0V$, $T_a = 27^{\circ}C$ unless otherwise noted

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit	Pins
Z-phase output signal rise time	t_r	$SWV = L$, $C_L = 15 \text{ pF}$ $0.1V_{DD} \rightarrow 0.9V_{DD}$	-	-	100	ns	VZ
Z-phase output signal fall time	t_f	$SWV = L$, $C_L = 15 \text{ pF}$ $0.9V_{DD} \rightarrow 0.1V_{DD}$	-	-	100	ns	VZ

8.4. Standard Scale Conditions

Electrical characteristics established under the following conditions.

Parameter		Conditions	Unit
Scale	Reflecting surface reflectance	50	%
	Non-reflecting surface reflectance	10	%
	Incremental pattern	20 μ m pitch, (10 μ m Cr line/10 μ m space)	-
	Z-phase pattern	30 μ m Cr line	-
Mounting	Gap (Δ Gap)	0.5	mm
	Yaw angle ($\Delta\theta_y$) Roll angle ($\Delta\theta_r$) Pitch angle ($\Delta\theta_p$)	0	°

9. FUNCTIONAL DESCRIPTION

The SMD-04B projects visible LED light with a center wavelength of 650nm through a slit and onto a scale with a 20μm pitch pattern (10μm line/10μm space), and detects the relative movement between the head and the scale by receiving a diffracted image of the reflected light using a photodiode detector. It outputs analog signals (sine wave) with 20μm period comprising an A-phase (VA) and B-phase (VB) that are 90° out of phase. It can also output a Z-phase digital signal formed using a 30μm reflective pattern at one location on the 20μm pitch scale. The Z-phase signal is output when the A-phase is HIGH ($V_A > V_{ref1}$ (or V_{ref2})).

To enable the Z-phase signal, set the LED brightness switching function to APC as described in section 9.1.

9.1. LED Brightness Switching Function

The SMD-04B has a function that adjusts the variation in signal amplitude by adjusting the LED brightness. The signal amplitude can be adjusted by changing the LED current to control the LED brightness as shown in the following table.

For details about APC adjustment, see section 9.2.

SWL1	H		L	
SWL2	H	L	H	L
A,B-phase analog signal amplitude	×1.0	×1.8	APC	OFF

9.2. LED Auto Power Control (APC) Brightness Adjustment Function

The SMD-04B has a built-in APC (Auto Power Control), LED brightness automatic adjustment function. It detects the light reflected from the scale using the photodiode detector, and controls the LED current to adjust the brightness.

In addition to reducing the fluctuation in amplitude of the A-phase (VA) and B-phase (VB) signals due to fluctuations in LED brightness, it also reduces the temperature dependency of the signal amplitude by compensating for changes in LED brightness due to temperature variations.

9.3. Reference Voltage Switching Function

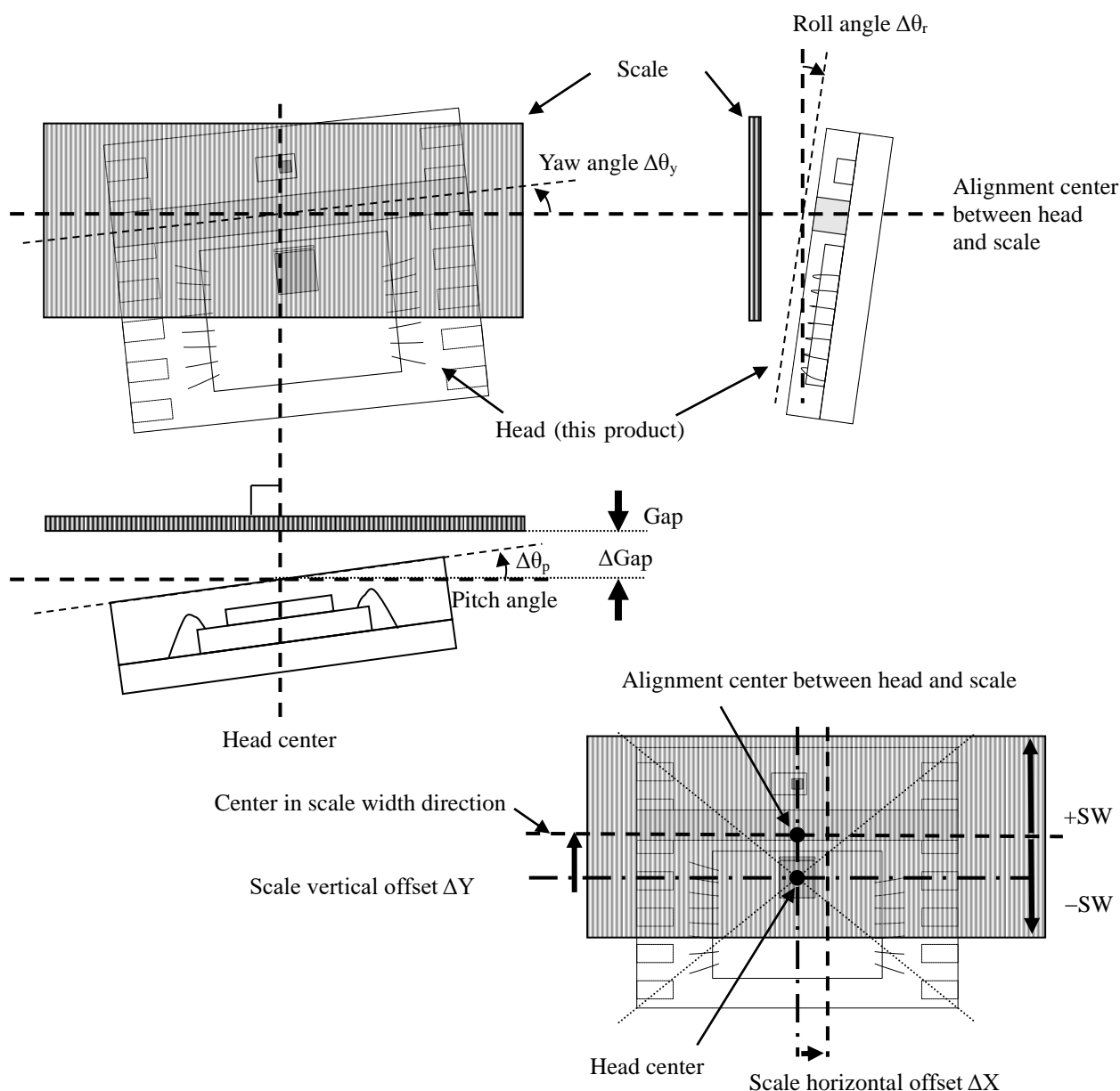
The SMD-04B has a function for switching the reference voltage (Vref) by changing the setting of the SWV pin. Set the reference voltage according to the required input conditions of the following stage circuit.

When used with SWV=H, the VZ pin becomes an open-drain output.

10. Scale Mounting Conditions

The optical center of the encoder head is offset from the physical center by 0.77 mm in the vertical direction (ΔY in the diagram below) and 0.0mm in the horizontal direction (ΔX in the diagram below). If using a linear scale, you only need to take the offset in the scale width direction (ΔY) into account. If using a rotary scale, the offset in the horizontal direction (ΔX) must also be taken into account.

The optimum optical conditions of the product will vary depending on the mounting conditions. Conduct careful evaluation to meet requirements. In particular, the effects on amplitude and phase difference due to mounting conditions are larger if using a small diameter rotary scale than if using a linear scale. Provision of separate mounting adjustments is recommended to achieve better product performance.



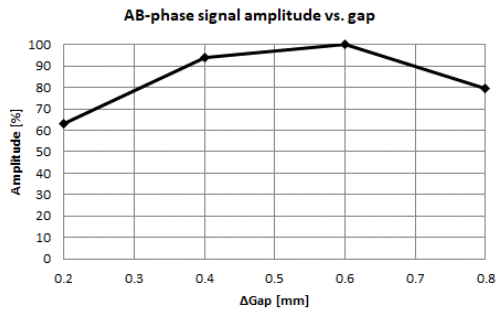
[Reference data]

	Parameter	Conditions	Unit
Scale	Scale width ($\pm SW$)	± 1 (min.)	mm
Mounting	Scale vertical offset (ΔY)	0.77	mm
	Scale horizontal offset (ΔX)	0.0	mm

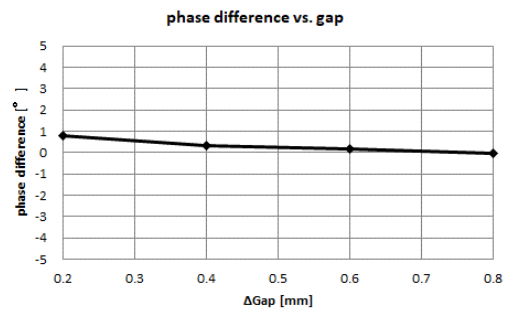
11. TYPICAL CHARACTERISTICS

* NPC standard scale and mounting conditions

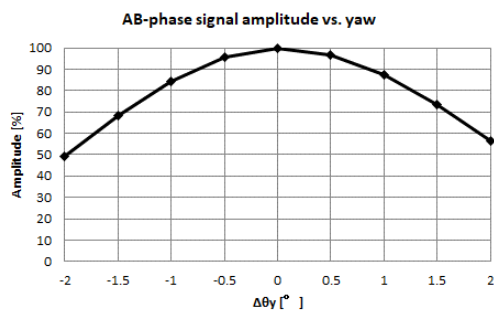
11.1. Mounting Characteristics



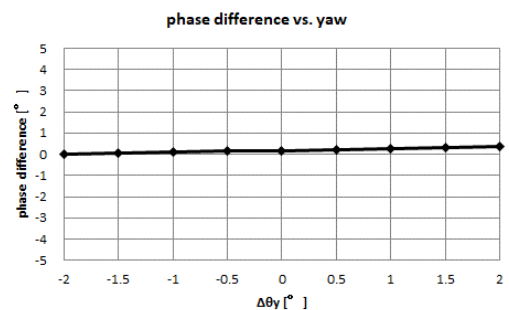
Amplitude v. Gap (ΔGap)



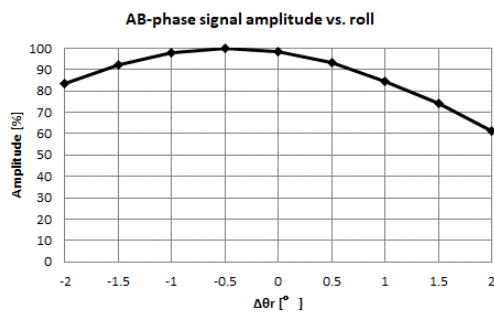
Phase difference v. Gap (ΔGap)



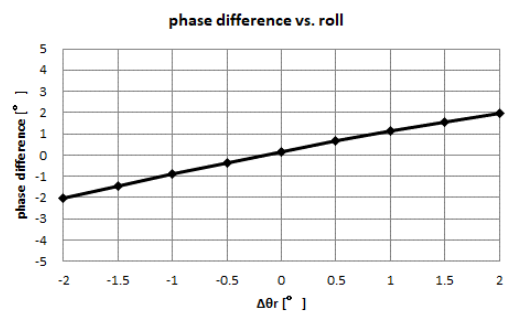
Amplitude v. Yaw angle ($\Delta\theta_y$)



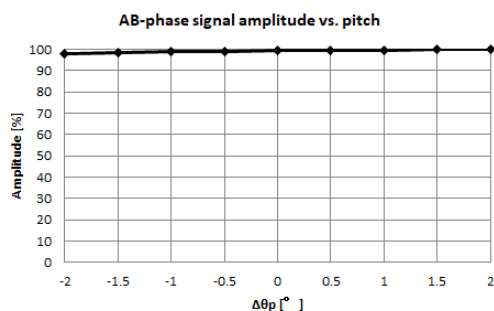
Phase difference v. Yaw angle ($\Delta\theta_y$)



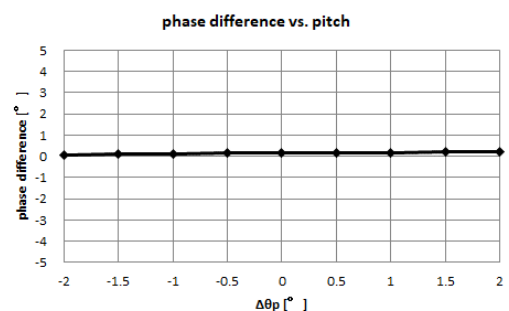
Amplitude v. Roll angle ($\Delta\theta_r$)



Phase difference v. Roll angle ($\Delta\theta_r$)

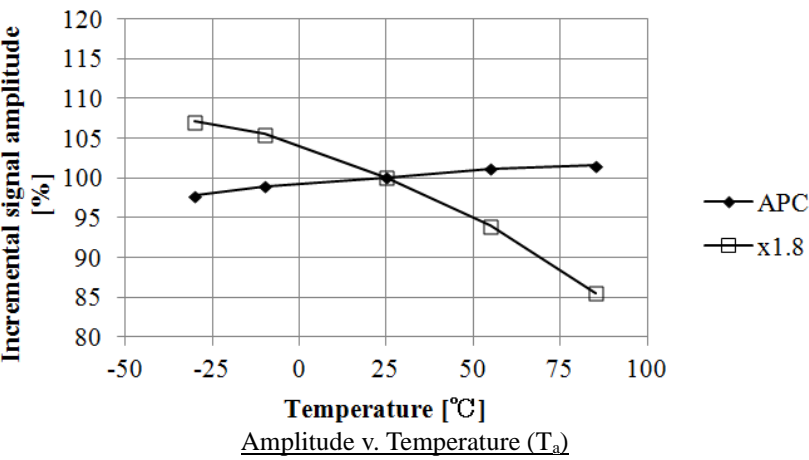


Amplitude v. Pitch angle ($\Delta\theta_p$)

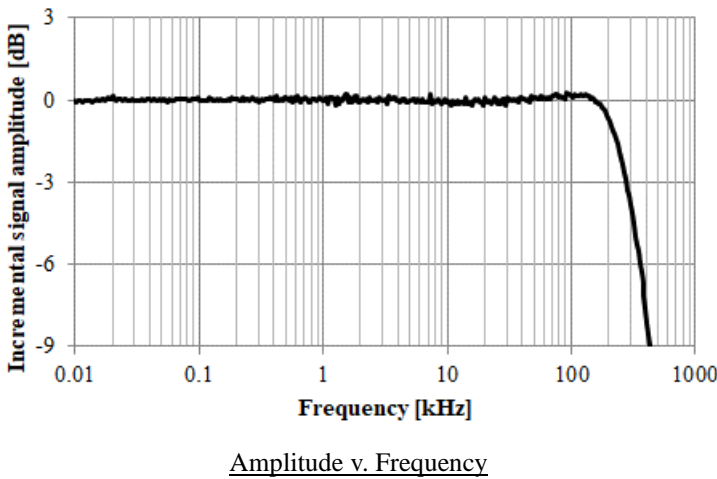


Phase difference v. Pitch angle ($\Delta\theta_p$)

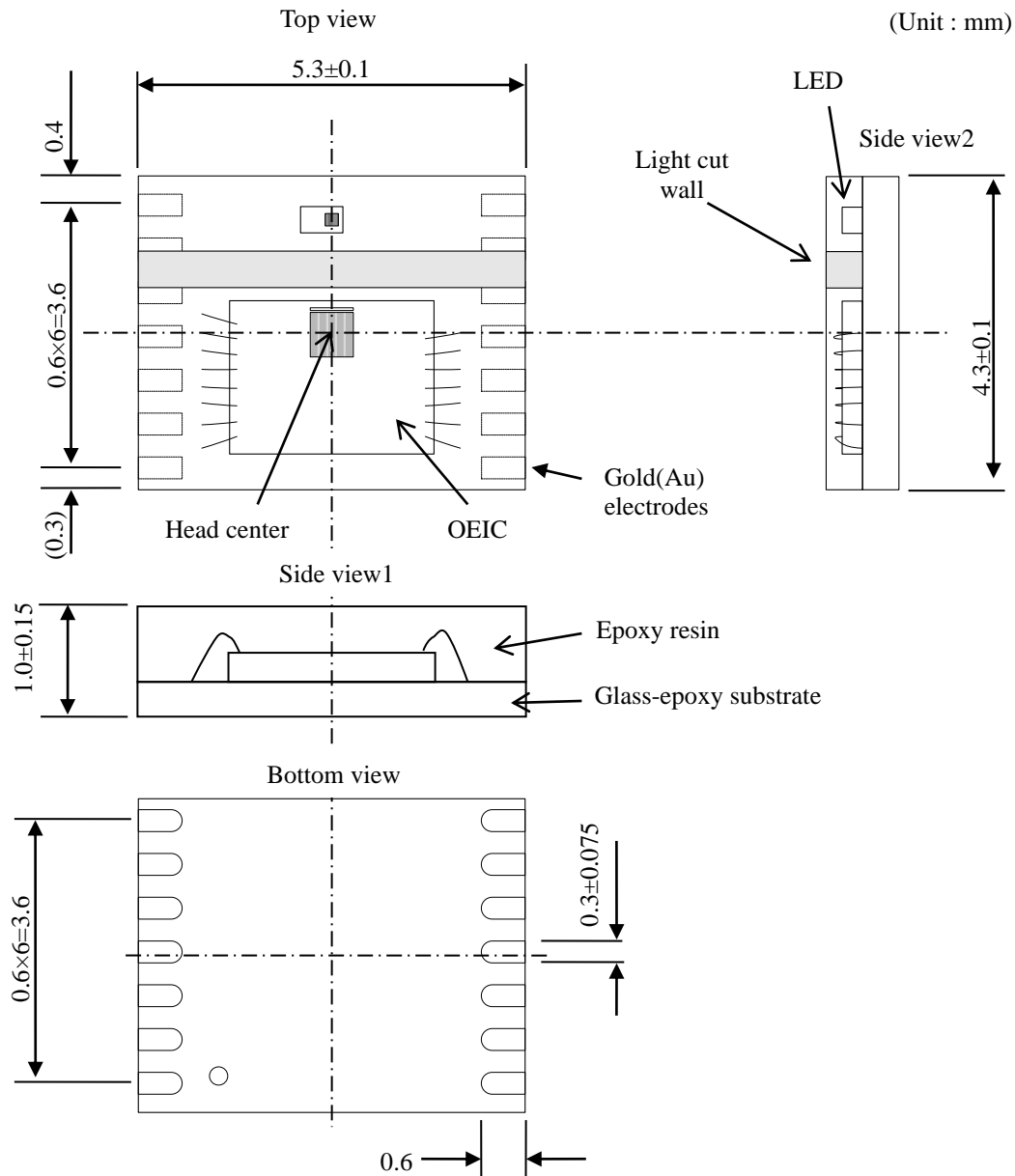
11.2. Temperature Characteristic



11.3. Frequency Response



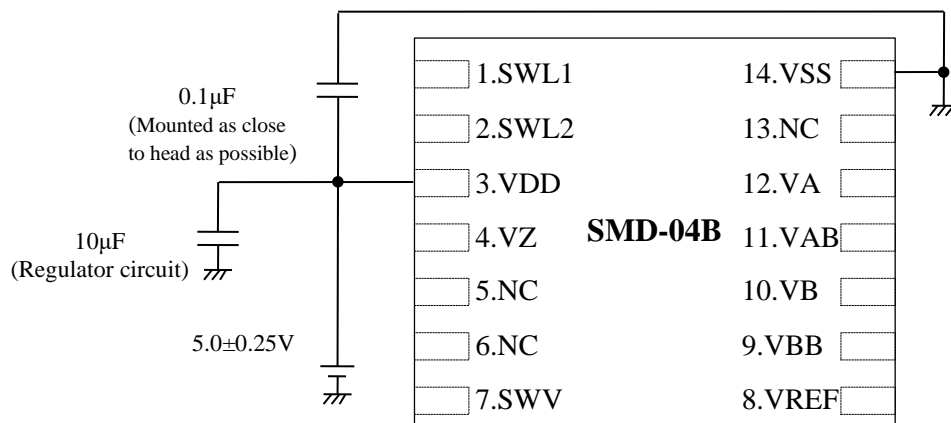
12. PACKAGE DIMENSIONS



13. USAGE NOTE

Keep this product and the wiring emerging from this product (specifically the analog signal wiring) as far away as possible from the wiring for the drive actuator. The susceptibility of the encoder signal to noise will increase the closer the wiring is positioned. Chattering of the digital signal may occur with increasing noise, adversely affecting position detection accuracy.

To reduce the effects of noise, connect capacitors to the VDD and VSS lines of the product as shown below.



14. USAGE AND PRECAUTIONS

This product is designed and manufactured to the generally accepted standards of reliability as expected for use in general electronic and electrical equipment, such as personal equipment, machine tools, and measurement equipment. This product is not designed and manufactured to be used in any other special equipment requiring extremely high level of reliability and safety, such as aerospace equipment, nuclear power control equipment, medical equipment, transportation equipment, disaster prevention equipment, security equipment.

If you wish to use this product in equipment requiring extremely high level of reliability, please contact our sales department or representative in advance.

In the event that this product is used in such equipment, please take scrupulous care and apply fail-safe techniques including redundancy and malfunction prevention in order to prevent damage to life, health, property, or infrastructure etc. in case there is some malfunction in the product.

Please pay your attention to the following points at time of using the products shown in this document.

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DE220201E 2022.08