NPC

OVERVIEW

The SM8142B is a transformer-less electroluminescent (EL) driver IC, capable of driving sheets up to 30cm² in size. It employs a high-efficiency driver output circuit configuration to control power dissipation. It is available in ultra-small 8-pin SON (Small Outline Non-leaded) packages*, making possible the construction of small, thin, low-power driver units.

* : SM8142BD

FEATURES

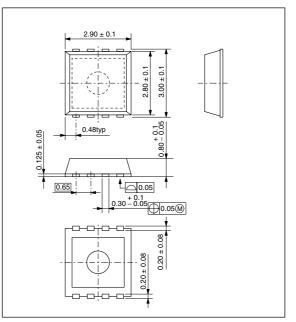
- Dedicated EL driver
- Noise-less smooth drive waveform
- High-efficiency output circuit
- Stand-by function
- Stable temperature characteristics
- Ultra-small package
- 1.6 to 5.5V supply voltage
- 0.3mA typ. (V_{DD} = 3.0V) current consumption (excluding coil current)
- 200Vp-p maximum EL driver voltage
- 31 to 1000Hz EL drive frequency range
- 0.22mH minimum coil inductance

ORDERING INFORMATION

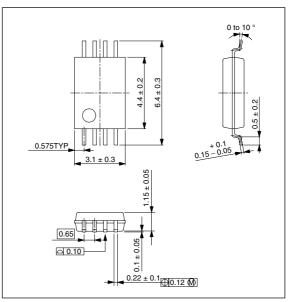
Device	Package
SM8142BD	8-pin SON
SM8142BV	8-pin VSOP

PACKAGE DIMENSIONS (Unit: mm)

8-pin SON



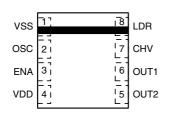
■ 8-pin VSOP



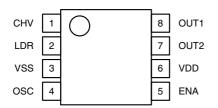
PINOUT (Top view)

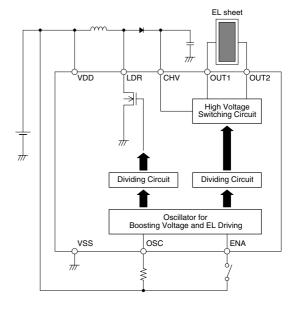
BLOCK DIAGRAM

■ 8-pin SON



■ 8-pin VSOP





PIN DESCRIPTION

Pin ni	umber	Name	I/O	Function			
VSOP-8	SON-8	Name	1/0	Function			
1	7	CHV	I	High-voltage DC input			
2	8	LDR	0	Booster coil driver output			
3	1	VSS	-	Ground			
4	2	OSC	I	bil and EL driver oscillator scillator frequency determined by external resistor)			
5	3	ENA	lp ¹	Enable input (HIGH: enable, LOW: disable)			
6	4	VDD	-	Supply			
7	5	OUT2	0	Output 2			
8	6	OUT1	0	Output 1			

1. Built-in pull-down resistor

SPECIFICATIONS

Absolute Maximum Ratings

Parameter	Symbol	Condition	Rating	Unit
Supply voltage range	V _{DD}		- 0.3 to 7.0	۷
Input voltage range	V _{IN}	All Input pins	V_{SS} – 0.3 to V_{DD} + 0.3	V
Output voltage	V _{CHV}	CHV pin	0.5 to 120	V
	V _{LDR}	LDR pin	0.5 to 120	V
	V _{OUT1/2}	OUT1, OUT2 pin	0.5 to 120	V
Power dissipation	PD	Ta ≤ 85°C	100	mW
Storage temperature range	T _{stg}		– 55 to 125	°C

Note. The device may be damaged or deteriorated if any of the above parameter ratings is exceeded.

Recommended Operating Conditions

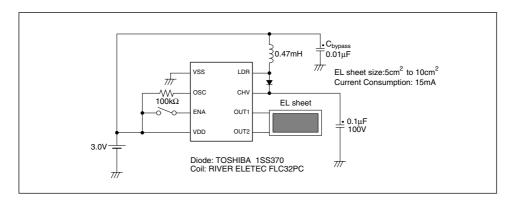
Parameter	Symbol	Condition		Unit			
		Condition	min	typ	max	Unit	
Supply voltage range	V _{DD2}		1.6	3.0	5.5	V	
Operating temperature	T _{OPR}		- 40	-	85	°C	
Operating ourrant ¹		Including coil current, V _{DD} = 3.0V	-	-	60	~ ^	
Operating current ¹	I _{DD2}	Including coil current, V _{DD} = 5.0V	-	-	36	mA	
	D	V _{DD} = 1.6 to 4.0V	-	0	-	kQ	
Current limit resistance	R _{CHV}	V _{DD} = 4.0 to 5.5V	10	20	-	kΩ	
	R _{OUT}	$EL \leq 30 cm^2$	-	0	-	kΩ	
		$EL \ge 30 cm^2$	-	1.0	-		
Coil inductance	L _{LDR}	f _{LDR} = 64kHz	-	0.47	-	mH	

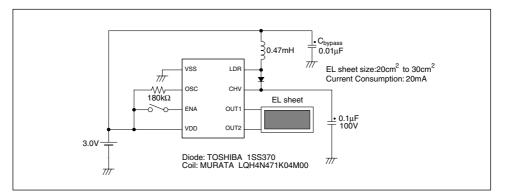
1. Max value is as same as Absolute Maximum Ratings.

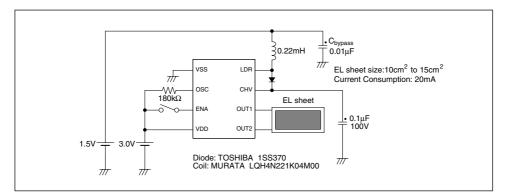
DC Characteristics

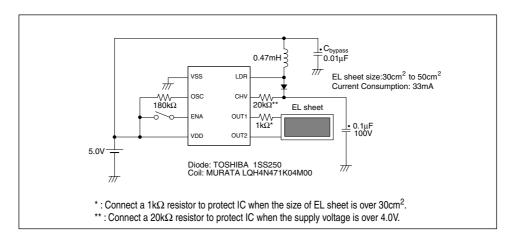
Parameter	Symbol	Condition	Rating			11-14
			min	typ	max	Unit
Supply voltage	V _{DD}		1.6	3.0	5.5	V
CHV output voltage	V _{CHV}		0.5	-	100	V
OUT1, OUT2 HIGH-level output voltage	V _{OUTH}		-	-	100	V
OUT1, OUT2 LOW-level output voltage	V _{OUTL}		-	-	0.5	V
LDR output resistance	R _{LDR}	I _{LDR} = 50mA	-	8.0	12.0	Ω
OSC oscillator frequency	f _{OSC1}	$R_{OSC} = 180 k\Omega$	205	256	307	kHz
OSC oscillator frequency range	f _{OSC2}		32	-	1024	
OUT1, OUT2 output frequency	f _{OUT1}	$R_{OCE}/R_{OSC} = 180k\Omega$	200	250	300	Hz
OUT1, OUT2 output frequency range	f _{OUT2}		31	-	1000	
LDR inductance driver frequency	f _{LDR1}	$R_{OCL}/R_{OSC} = 180k\Omega$	51	64	77	
LDR inductance driver frequency range	f _{LDR2}		8	-	256	kHz
ENA HIGH-level input voltage	V _{ENAH}	ENA = "H", V _{DD} = 1.6 to 5.5V	V _{DD} – 0.5	-	V _{DD} + 0.3	v
ENA LOW-level input voltage	V _{ENAL}	ENA = "L", V _{DD} = 1.6 to 5.5V	V _{SS} – 0.3	-	V _{SS} + 0.5	
ENA input current	I _{ENAH}	$V_{\text{ENAH}} = V_{\text{DD}} = 3.0 \text{V}$	2.0	4.0	6.0	μA
Operating current	I _{DD1}	Excluding coil current	-	-	0.5	mA
Stand-by current	I _{STB}	ENA = "L"	-	_	1.0	μA

TYPICAL APPLICATIONS









Note) Do not operate the SM8142B with the EL sheet NOT connected (no load to OUT1/OUT2) since the IC will be damaged.

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